

ISDN S-INTERFACE DUAL SMT TRANSFORMER

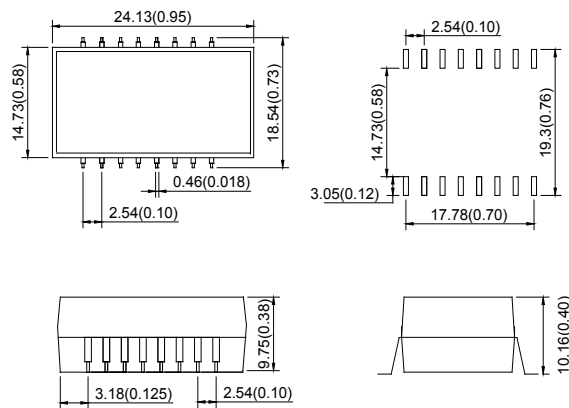
Features:

- Meets the pulse waveform template of CCITT 1.430
- Excellent longitudinal balance.
- Custom variations welcome.
- Isolation:1500 Vrms.

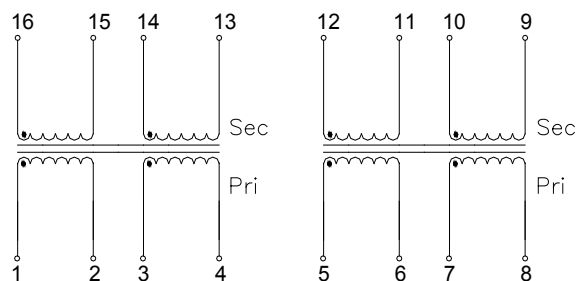
Specifications: at 25 degree C.

Part No	Turn Ratio ±2%		OCL Pri (mH min)	LL Sec (uH max)		Cww (pF.max)	CD Pri (pF.max)		DCR Pri (Ω±25%)		DCR Sec (Ω±25%)		△ I _{dc} (mA max)
	A	B	A & B	A	B	A & B	A	B	A	B	A	B	
MT65496	1:2.5	1:2.5	22	40	40	100	180	180	2.4	2.4	5.3	5.3	1
MT65497	1:2	1:2	22	11	11	100	180	180	2.4	2.4	4.4	4.4	1
MT65498	1:2	1:2	22	15	15	85	80	80	2.4	2.4	4.2	4.2	1
MT65499	1:1	1:1	22	5	5	100	42	42	2.4	2.4	2.4	2.4	1
MT65500	1:1	1:2	22	5	15	100	42	80	2.4	2.4	2.4	4.2	1
MT65501	1:1.8	1:1.8	22	15	15	100	160	160	2.4	2.4	3.8	3.8	1

Dimensions: mm(inch)±0.25(0.010)



Schematics:



ISDN S-INTERFACE DUAL SMT TRANSFORMER

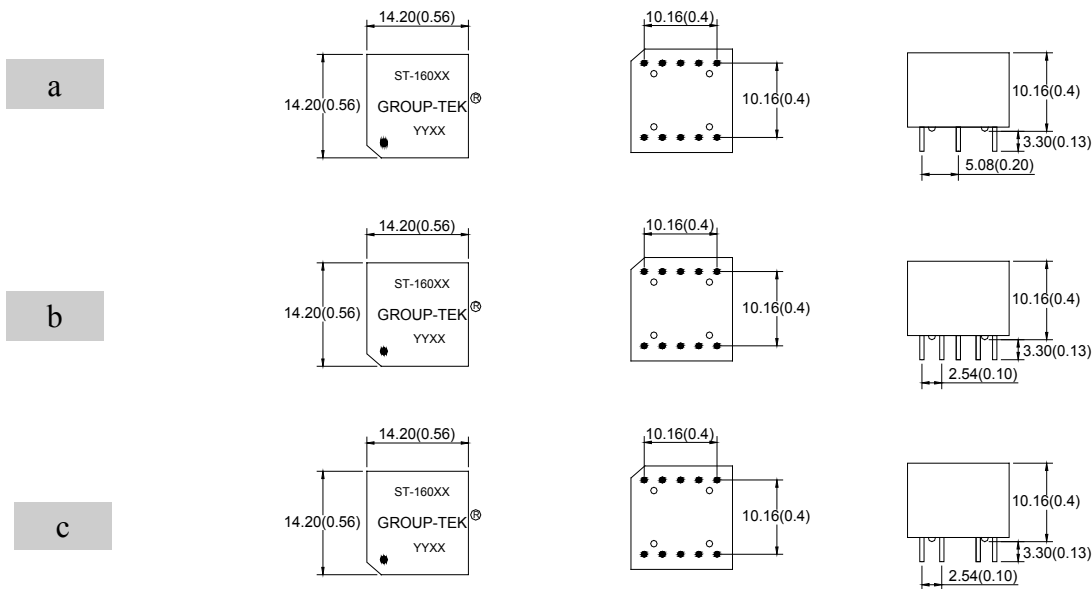
Features:

- Meets the pulse waveform template of CCITT 1.430
- Excellent longitudinal balance.
- Custom variations welcome.
- Isolation:1500 Vrms.

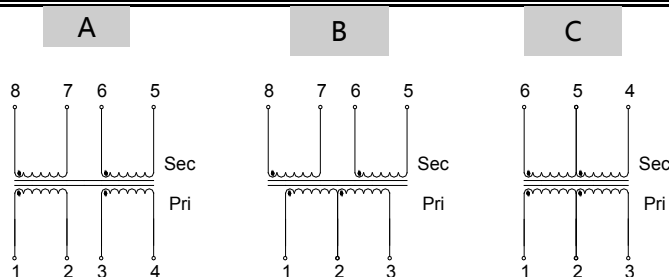
Specifications: at 25 degree C.

Part No	Turn Ratio ±2%	OCL Pri (mH min)	LL Sec (uH max)	Cww (pF.max)	CD Pri (pF.max)	DCR Pri (Ω±25%)	DCR Sec (Ω±25%)	Δ Idc (mA max)	Package/ Schematic
MT64999	1CT:2CT	22	11	100	180	2.4	4.4	1	c/A
MT64998	1CS:2.5CS	22	15-40	100	110	2.5	5.8	1	a/C
MT64996	1CT:2.5CS	22	30	100	150	2.5	5.8	1	b/B
MT64995	1CT:2CT	22	15	100	80	2.5	4.3	1	c/A
MT64994	1CT:1.8CS	22	15	100	160	2.5	4.2	1	b/B
MT64993	1CT:1CT	22	5	100	42	2.4	2.4	1	c/A

Dimension:mm(inch)+/-0.25(0.010)



Schematic:



ISDN S-INTERFACE THROUGH HOLE DUAL TRANSFORMERS P/N:MT65495

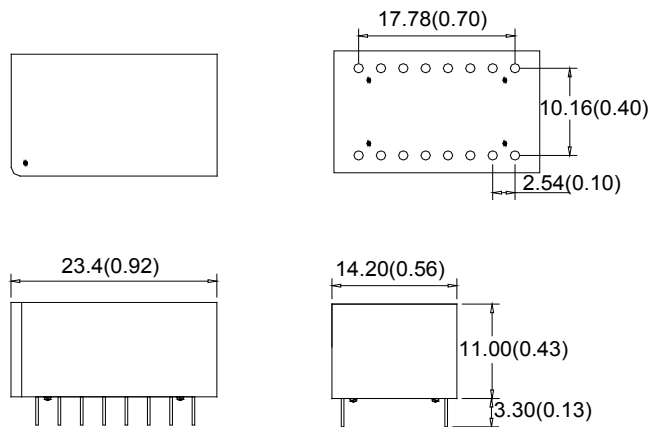
Features:

- Meets the pulse waveform template of CCITT 1.430
- when recommended transformer and chip pair is used.
- Cost effective.
- Custom variations welcome.

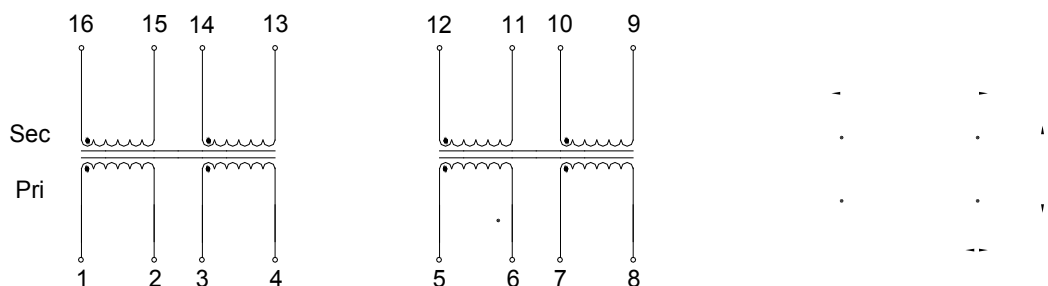
Specifications: at 25 degree C.

- Inductance: 22mH min. (@10KHz, 0.1Vrms Pri.)
- Leakage inductance: 15uH max. (@100KHz, 0.1Vrms) (Pri. Shorted)
- Interwinding cap: 120pF max. (@100KHz, 0.1Vrms)
- D.C. Resistance: Pri $2.5\Omega \pm 25\%$.
Sec $4.4\Omega \pm 25\%$.
- Choke inductance: 40uH/line (Typ. @100KHz, 0.1Vrms)
- Isolation: 1500Vrms.

Dimensions: mm(inch) +/- 0.25(0.010)



Schematics:



ISDN S-INTERFACE THROUGH HOLE DUAL TRANSFORMER

Features:

- Meets the pulse waveform template of CCITT 1.430
- Compatible with industry standard footprints.
- Developed for enhanced EMC performance.
- Custom variations welcome.
- Isolation: 1500Vrms.

Specifications: at 25 degree C.

Part No	Transformer							Choke		Δ Idc mA max	Secondary Pins	Schematic
	Ratio \pm 2% Pri:Sec	OCL Pri (mH min)	LL Sec (uH max)	C _{ww} (pF max)	CDPri (pF max)	DCR Pri (Ω \pm 25%max)	DCRSec (Ω max)	OCL (mH min)	DCR Ω			
MT5011	1CT:1CT	30	10	150	100	3.2	3.2(+25%)	4.7	1.2	5	9-7,10-12	B
MT5012	1CT:2CT	30	30	150	100	3.2	5.4(+25%)	4.7	1.2	5	9-7,10-12	B
MT5013	1CT:2.5CT	30	30	150	110	3.2	9.0	4.7	1.2	5	9-7,10-12	B
MT5015	1CT:2	30	30	150	100	3.2	5.4(+25%)	4.7	1.2	5	9-10,8-11	A

Dimensions: mm(inch) \pm 0.25(0.010)

Schematics:

